Form PTO-1449 (Rev. 2-32)	U.S. Department of Commerce Patent & Trademark Office	Atty. Docket No. Q53403	Serial No. 09/242,822
INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)		Applicant: Georges FICHE	
		Filing Date: February 24, 1999	Group:

U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Sub-Class	Filing Date (if appropriate)

OIPE JC36
JAN 05 2000
PATENT & TRADEMARK OFFICE

RECEIVED
JAN 6 - 2000
Group 2700

FOREIGN PATENT DOCUMENTS

		Document	Date	Country	Class	Sub-class	Translation Yes/No
DMP		EP 0 705 006 A1	04/03/96	European			Yes

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

DMP		Catania, C. et al.: "Analysis of Replicated Banyan Networks with Input and Output Queueing for ATM Switching" 1996 IEEE International Conference on Communications (ICC) Converging Technologies for Tomorrow's Applications, Dallas, June 23-27, 1996, vol. 3, June 23, 1996, Institute of Electrical & Electronics Engineers, Pages 1685-1689
DMP		Lea, C-T: " Buffered or Unbuffered: A Case Study Based on LOGD (N,E,P) Networks" IEEE Transactions on Communicaions, vol. 44, no. 1, January 1, 1996, pages 105-113, XP000549647
DMP		Ferrari, G. et al.: "Distributed Routing Techniques for Internally Inbuffered Interconnection Networks" European Transactions on Telecommunications and Related Technologies, vol. 4, no. 1, January 1, 1993, pages 85-94, XP000358892
DMP		Wong, P.C. et al.: "A Large Scale Packet Switch Interconnection Architecture Using Overflow Switches" Proceedings of the International Conference on Communications (ICC), Geneva, Mary 23-26, 1993, vol. 2, Institute of Electrical Engineers,
DMP		Svinnset I: "Nonblocking ATM Switching Networks" IEEE Transactions on Communications, col. 42, no. 2/03/04, February 1, 1994, pages 1352-1358, XP000447383
DMP		Jajszczyk, A. et al: "Optimum Structures and Growability of Shared-Buffer Fabrics" Proceedings of the Global Telecommiunications Conference (Globecom) San Francisco, Nov. 28-Dec. 2, 1994, pages 309-313, XP000488563 Institute of Electrical and Electronics Engineers
DMP		Takashi Shimizu et al: "A Cost-Effective Network for Very Large ATM Cross-Connects- The Delta Network with Expanded Middle Stages" IEICE Transactions on Communications, col. E77-B, no. 11, November 1, 1994, pages 1429-1436, XP000504590

EXAMINER:	DATE CONSIDERED: 9-12-2002

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication.